

Semiconductor Diodes

Name: _____ Section: 4BL-____ Date performed: ____/____/____

Lab station: _____ Partners: _____

Circuit box #____ Oscilloscope #____

Part A

Circuit diagram:

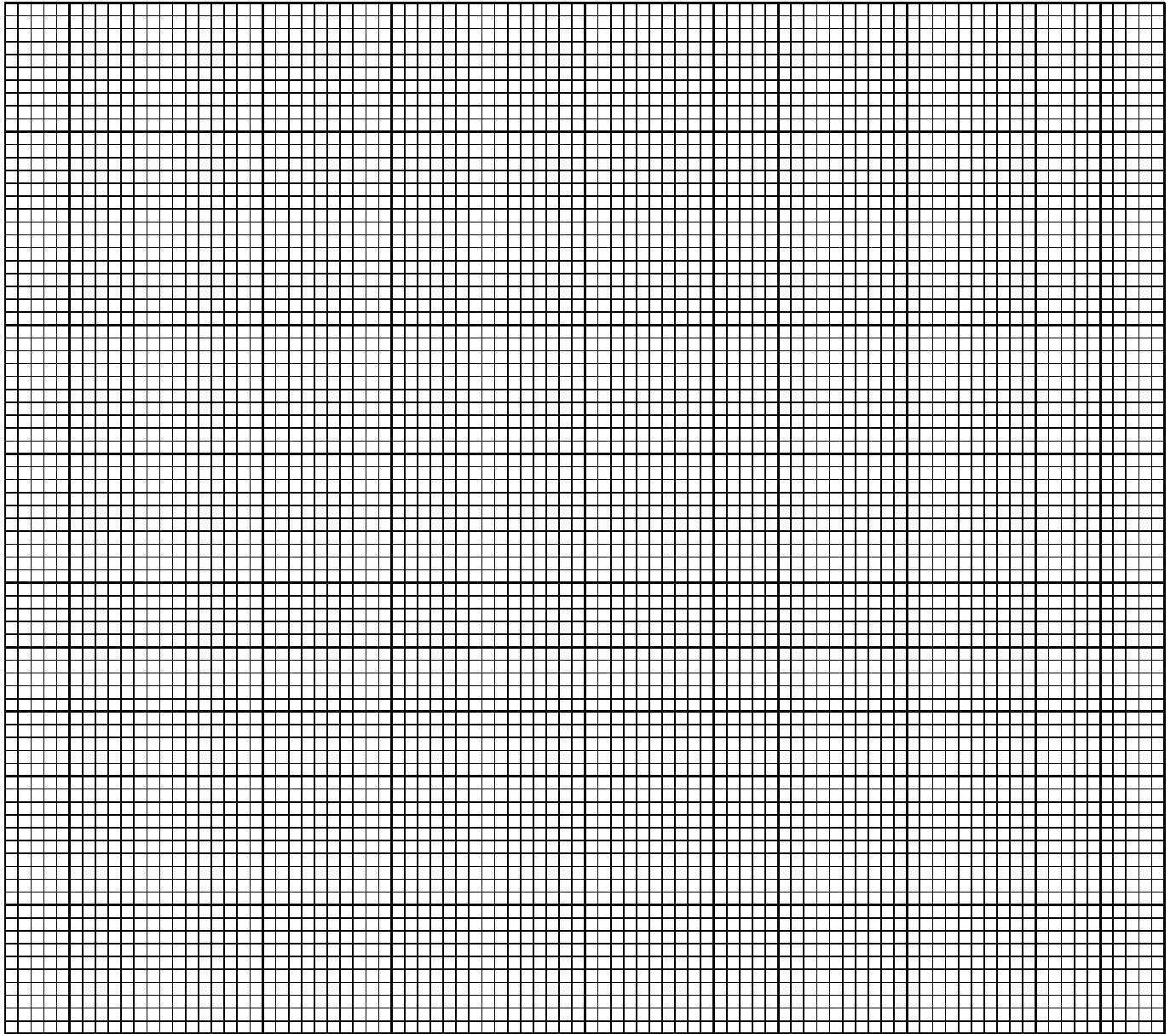
V () (range)	I () (range)
()	()
()	()
()	()
()	()
()	()
()	()
()	()

V () (range)	I () (range)
()	()
()	()
()	()
()	()
()	()
()	()
()	()

Plot V vs. I on the next page.

barrier voltage = _____ min fwd resistance = _____

Calculations:



Part D

Circuit diagram:

Settings:

V_S (Ch____): _____ V/div mode: DC / AC

V_R (Ch____): _____ V/div mode: DC / AC

V_D (Ch____): _____ V/div mode: DC / AC

Timing: _____ s/div frequency dial = _____ Hz

Plot V_S , V_R , and V_D vs. t on the next page (three graphs stacked with equal times aligned).

V_R flattened at _____ V. Explain:

V_D flattened at _____ V. Explain:

